# **Code Generation**

Daniel H. Luecking MASC

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$$\begin{aligned} (110\,011) &= (100\,101) + (010\,110) \text{ and} \\ (111\,000) &= (100\,101) + (010\,110) + (001\,011). \end{aligned}$$

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[To be able to discuss general cases we need a notation for the strings with a single 1 in them. So we let  $e_i$  stand for the string of all 0s except for a 1 in position j.]

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$$G = \left(\begin{array}{ccc|ccc|ccc} 1 & 0 & 0 & 1 & 0 & 1 \\ 0 & 1 & 0 & 1 & 1 & 0 \\ 0 & 0 & 1 & 0 & 1 & 1 \end{array}\right)$$

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$$(110)G = (100101) + (010110) = (110011)$$

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## **Structure of the Generating matrix**

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$$(w_1 \, w_2 \, w_3 \, w_4) \begin{pmatrix} 1 & 0 & 0 & 0 & 1 & 0 & 1 \\ 0 & 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 0 & 1 & 0 & 0 & 1 & 1 \\ 0 & 0 & 0 & 1 & 1 & 1 & 1 \end{pmatrix}$$

$$= (w_1 \, w_2 \, w_3 \, w_4 (w_1 + w_2 + w_4) (w_2 + w_3 + w_4) (w_1 + w_3 + w_4))$$

The extra bits, for example  $w_1 + w_2 + w_4$  in the 5th position, are called parity bits because the effect they have is that the bits of wG in certain positions must have an even number of 1s.

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$$r_1 + r_2 + r_4 = r_5$$

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This is easier to coordinate if we rearrange it, using the fact that in  $\mathbb{Z}_2$ , x + x is always zero.

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$$egin{array}{ccccc} r_1+r_2 & +r_4+r_5 & =0 \\ r_2+r_3+r_4 & +r_6 & =0 \\ r_1 & +r_3+r_4 & +r_7=0 \end{array}$$

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These equations can be written in matrix form as

$$\begin{pmatrix} 1 & 1 & 0 & 1 & 1 & 0 & 0 \\ 0 & 1 & 1 & 1 & 0 & 1 & 0 \\ 1 & 0 & 1 & 1 & 0 & 0 & 1 \end{pmatrix} \begin{pmatrix} r_1 \\ r_2 \\ r_3 \\ r_4 \\ r_5 \\ r_6 \\ r_7 \end{pmatrix} = \begin{pmatrix} 0 \\ 0 \\ 0 \end{pmatrix}$$

The equation on the previous slide could also be rewritten by taking the transpose:

$$(r_1 \ r_2 \ r_3 \ r_4 \ r_5 \ r_6 \ r_7) \begin{pmatrix} 1 & 0 & 1 \\ 1 & 1 & 0 \\ 0 & 1 & 1 \\ \hline 1 & 1 & 1 \\ \hline 1 & 0 & 0 \\ 0 & 1 & 0 \\ 0 & 0 & 1 \end{pmatrix} = ( \ 0 \ \ 0 \ \ 0 \ )$$

But our book has chosen to use the other way.

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$$\begin{pmatrix} a & b & c \\ d & e & f \end{pmatrix}^{\text{tr}} = \begin{pmatrix} a & d \\ b & e \\ c & f \end{pmatrix}.$$

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To save space, we often write a single column matrix, like the one containing  $r_1$  through  $r_7$  on the previous slide, as the transpose of a row:  $(r_1r_2r_3r_4r_5r_6r_7)^{\rm tr}$ .

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$$G = \left(\begin{array}{ccc|c} 1 & 0 & 0 & 1 & 1 & 0 \\ 0 & 1 & 0 & 1 & 1 & 1 \\ 0 & 0 & 1 & 1 & 0 & 1 \end{array}\right) \quad \text{then} \quad A = \left(\begin{array}{ccc|c} 1 & 1 & 0 \\ 1 & 1 & 1 \\ 1 & 0 & 1 \end{array}\right)$$

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And so,

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ight) \quad ext{and} \quad H = \left( egin{array}{cccc} 1 & 1 & 1 & 1 & 0 & 0 \\ 1 & 1 & 0 & 0 & 1 & 0 \\ 0 & 1 & 1 & 0 & 0 & 1 \end{array} 
ight)$$

Now suppose we and a remote site have arranged to use this code to send 3-bit messages, and I want to send w=(101).

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Since the result is not  $(000)^{tr}$  they know there is an error.

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But if they know (or assume) that e has only a single 1, then by our rules for matrix multiplication  $He^{\rm tr}$  will be the column of H corresponding to the position of the 1 in e. In our actual example  $Hr^{\rm tr}=He^{\rm tr}=(101)^{\rm tr}$  is the 3rd column of H. So that pinpoints the error.

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- 4. In case 1 or 2, they have the correct code word; the original message is found by removing the parity bits that were added by the encoding.

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If we need only one-bit error correction it can be shown that the number of bits to add to m-bit messages is a little more than  $\log_2 m$ . For example, 64-bit messages need add only 7 bits. Then  $G=(I\mid A)$  has 64 rows and 7 columns where I has 64 rows and columns and A has 64 rows and 7 columns. Then  $H=(A^{\mathrm{tr}}\mid I)$  is 7-by-71 with a 7-row by 64-column  $A^{\mathrm{tr}}$  and 7 by 7 identity I. [Note that the two identity matrices are rarely the same size.]

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Of course, this scheme corrects only one-bit errors, probably not enough for reliable transmission of 64 bits at a time. Setting up one that corrects more errors is beyond the scope of this course.